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				First Named Inventor	Tsern et al.
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Examiner Name	Cheung, Mary Da Zhi Wang				
Sheet	1	of	1	Attorney Docket Number	60809-0146-US

U.S. PATENT DOCUMENTS								
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate	
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FOREIGN PATENT DOCUMENTS								
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me		B'URSKY, "Memory-CPU Interface Speeds Up Data Transfers," Electronic Design, pp. 137-142, March 19, 1992.						
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		GARY et al., "The PowerPC™ 603 Microprocessor: A Low-Power Design for Portable Applications," IEEE Design & Test of Computers, Winter, 1994.						
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Examiner Signature	<i>Mary Zhi Wang</i>	Date Considered	9-28-07
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